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File 348:EUROPEAN PATENTS 1978-2003/Nov W02
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File 349:PCT FULLTEXT 1979-2002/UB=20031106,UT=20031030
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File 350:Derwent WPIX 1963-2003/UD,UM &UP=200373
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Set	Items	Description
S1	47	AU=(ALOWERSSON, J? OR ROSLUND, B? OR SUNDSTROM, P? OR ALO- WERSSON J? OR ROSLUND B? OR SUNDSTROM P?)
S2	1	S1 AND SERIAL()FORMAT

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2/5,K/1 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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013376005 **Image available**
WPI Acc No: 2000-547943/200050
XRPX Acc No: N00-405340

Converter for data in serial and parallel format, has twin port storage cells linked to data channels via database with buffer circuit

Patent Assignee: SWITCHCORE AB (SWIT-N)
Inventor: ALOWERSSON J ; ROSLUND B ; SUNDSTROEM P
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
SE 9804479	A	20000623	SE 984479	A	19981222	200050 B
SE 518865	C2	20021203	SE 984479	A	19981222	200304

Priority Applications (No Type Date): SE 984479 A 19981222

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
SE 9804479	A	23	H04Q-011/04	
SE 518865	C2		H04Q-011/04	

Abstract (Basic): SE 9804479 A

NOVELTY - Each storage cell in the storage device (30) associated with each serial data channel (20) has two ports, all of the first ports in one storage device being coupled in parallel to a database linking the device to the associated data channel. The database includes at least one buffer circuit for separating the database into sections, each of which is coupled to the first port of one or more storage cells in each storage device vector. Means (100) are provided for allowing data to be transferred between the database and at least one storage cell via the first ports, and to allow the transfer of data from one database section to an adjacent section via at least one buffer circuit. DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for: (a) a method for converting serial format data to parallel format data (and vice versa) using this device; and (b) a communication exchange containing this converter. COMPUTING AND CONTROL - The means for allowing data transfer from the database to the storage cell, and between database sections, comprises a first clock generating device controlling access to the storage cell, and controlling the transfer of data between adjacent database sections. The storage cells are two-port random access memory (RAM) cells. The first and second ports are in and out-ports respectively, or vice versa.

USE - For telecommunication exchange systems handling both synchronous and asynchronous data, and for (de)multiplexing and synchronising multiple information streams.

ADVANTAGE - The converter can be run at high capacity due to the less complicated selection of time intervals in between writing and reading in the storage structure. The converter has a relatively simple and flexible structure. DESCRIPTION OF DRAWING(S) - The drawing shows a serial to parallel format data converter. (10) Converter; (20) Channels with 16 bit width for sending data formatted in ATM cells; (30) Temporary storage device; (40) Read amplifier; (100) Write control unit; (200) Read control unit.

Dwg.1/6

Title Terms: CONVERTER; DATA; SERIAL; PARALLEL; FORMAT; TWIN; PORT; STORAGE ; CELL; LINK; DATA; CHANNEL; DATABASE; BUFFER; CIRCUIT

Derwent Class: T01; W01; W02

International Patent Class (Main): H04Q-011/04

File Segment: EPI

data bus
data bus

Inventor: ALOWERSSON J ...

... ROSLUND B

...Abstract (Basic): buffer circuit. DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for: (a) a method for converting **serial format** data to parallel format data (and vice versa) using this device; and (b) a communication...

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